

**PATENT APPLICATION
DOCKET #1263-0025US**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

DEEPAK SABHARWAL et al.

Serial No.: Unassigned

Filed: March 30, 2003

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Group Art Unit: Unassigned

Examiner: Unassigned

For: WORDLINE-BASED SOURCE-BIASING SCHEME FOR REDUCING MEMORY CELL
LEAKAGE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Sir:

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Pursuant to 37 CFR §1.56 and in accordance with 37 CFR §§1.97-1.99, information relating to the above-identified application is hereby disclosed. Copies of each of the references listed on the attached Form PTO/SB/08 (A or B or both) are enclosed herewith, subject to the Notice of Waiver issued by the USPTO on July 11, 2003. Certain of the references may contain markings, underlinings or other notations. These markings are not intended and should not be construed as

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Respectfully submitted,



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Date: March 30, 2004

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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known	
		Application Number	
		Filing Date	March 30, 2004
		First Named Inventor	Deepak Sabharwal et al.
		Art Unit	
		Examiner Name	
Sheet 1	of 1	Attorney Docket Number	1263-0025US

OTHER PRIOR ART–NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		KENICHI OSADA et al.; "16.7fA/cell Tunnel-Leakage-Suppressed 16MB SRAM for Handling Cosmic-Ray-Induced Multi-Errors"; 2003 IEEE International Solid-State Circuits Conference; Pages 302-303.	

Examiner Signature		Date Considered	
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